

Unlocking Transformative Alwith Photonic Computing

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4 Co-Founders in a team of 25



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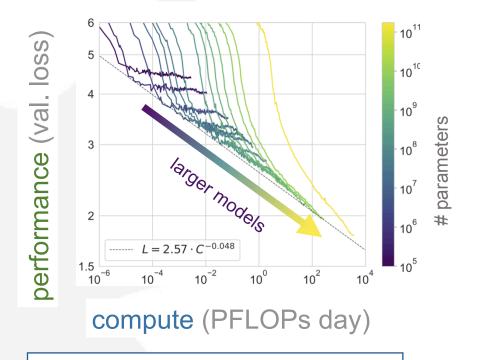


The Al scaling hypothesis



Scaling laws in Language Models [Kaplan et al., 2020]

With strong and scalable priors, increased model size is all you need!



Larger models score higher, generalize better, train faster

The biggest lesson that can be read from 70 years of AI research is that general methods that leverage computation are ultimately the most effective, and by a large margin.

Rich Sutton, The Bitter Lesson.

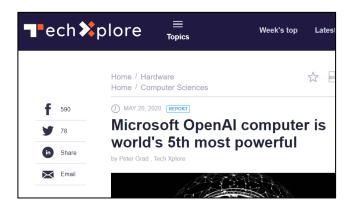
Reaching the limits?

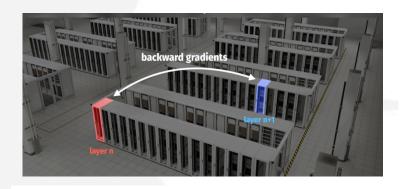


Impressive performance boost of GPT-3 over GPT-2 in only a year!

Same Transformer-based modelbut BIGGER

- Amount of training data x50 « just » linear
- Number of parameters x110





The challenge: Superlinear Scaling!

++ raw compute

++ memory

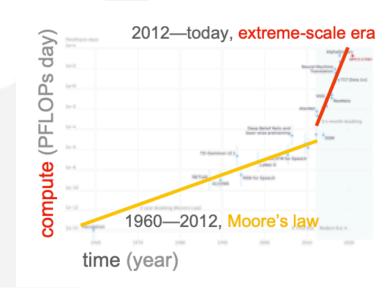
++++ communication between compute nodes

Scaling laws → training **a truly scaled-up « GPT-4 »** (10 trillion parameters) would require a 8,000 A100 GPUs supercomputer **running for 17 years**

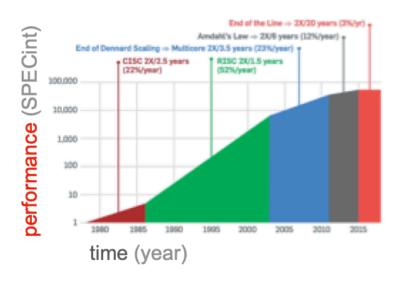
Transformative AI will not happen with silicon only



How to reconcile deep learning gargantuan needs with diminishing hardware gains?



Modern deep learning significantly outpaces Moore's Law



Hardware progress is increasingly bound to specialization

The computing hardware problem is an algorithm problem



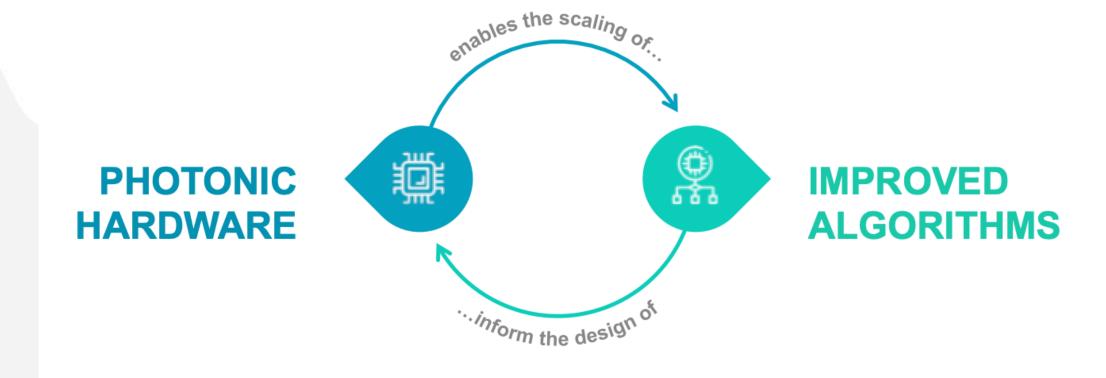
The Hardware Lottery

Sara Hooker — August, 2020



"How does tooling choose which research ideas succeed and fail, and what does the future hold?"

Hardware at the speed of software



What is LightOn OPU?





LightOn OPU is the world's first million-scale photonic co-processor dedicated to massive scale Al models





CPU,GPU, TPU, IPU, etc...

Disruptive electro-photonics hardware *OPU: Optical Processing Unit with Photonic Cores*

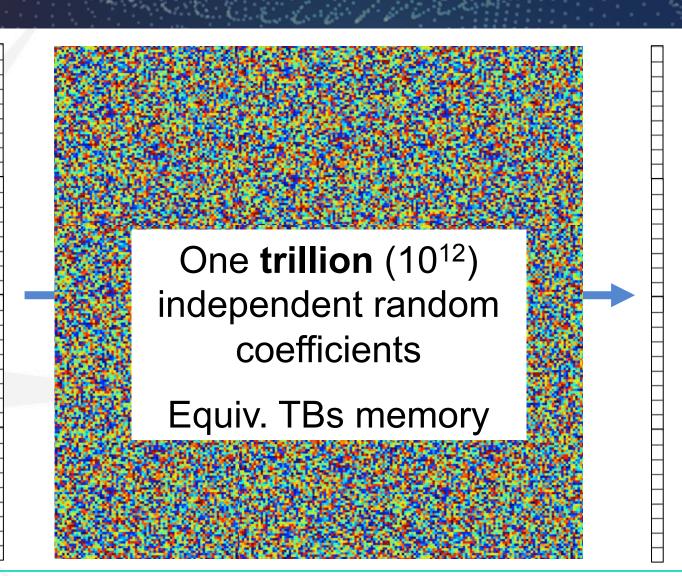


Optimized software platform compliant with Python and ML packages



Matrix-vector multiplication through light scattering

1 million independent input pixels



1 million independent output pixels

Designed for Large Scale Machine Learning

The OPU performs **Random Projections** in the analog domain input vector $x \rightarrow output vector y = Hx$

with H a complex random iid matrix

Analog - <u>non Von Neumann</u> – operations

Introducing LightOn Appliance

World's first commercially available photonic co-processor for AI and HPC



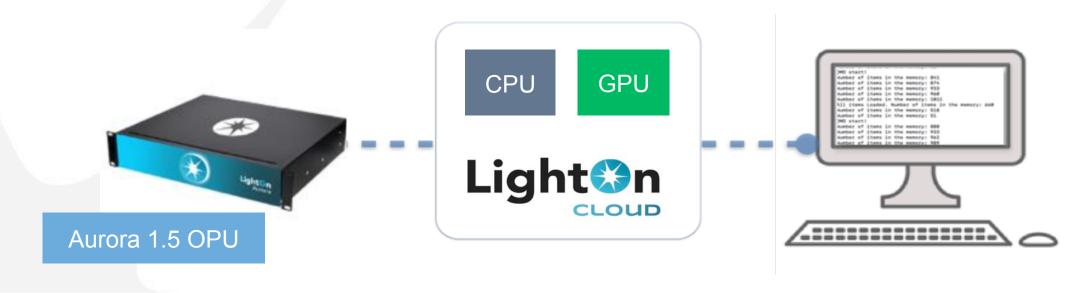


on-premises rackable 2U unit, external PCIe connected 1,500 TeraOPS at 30 W TDP

Spec sheet and leasing packages at

https://lighton.ai/lighton-appliance/

Test drive *today* on the LightOn Cloud

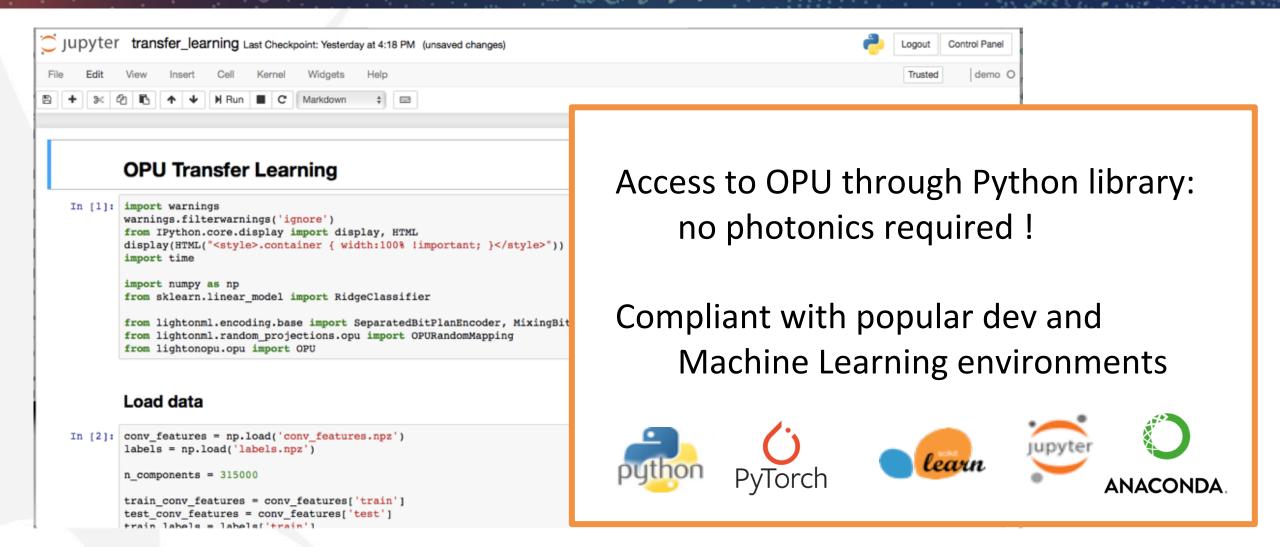


- Hybrid cloud platform hosted in 2 datacenters in Paris area continuously upgraded since Dec 2017
- Researchers get free compute time through LightOn Cloud for Research program
- See all info on https://cloud.lighton.ai

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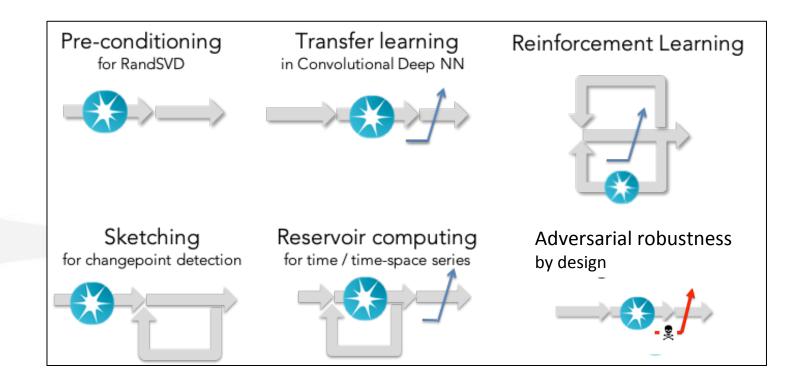
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User interface (Jupyter)



Hybrid computing in Al pipelines





Hybrid computing in Al pipelines



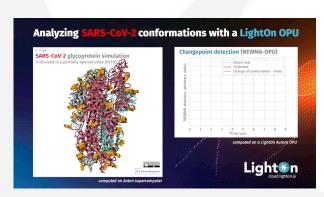


Ivan Dokmanic Associate Prof.









Accelerating SARS-COv2 Molecular Dynamics Studies with Optical Random Features

Amélie Chatelain LightOn ML R&D engineer







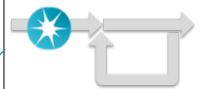
Transfer learning in Convolutional Deep NN



Reinforcement Learning



Sketching for changepoint detection



Reservoir computing for time / time-space series



Adversarial robustness by design





Collaboration

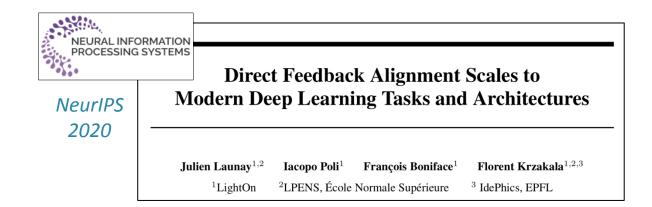


NeurIPS 2020

A new paradigm for Al training

BEYOND BACKPROPAGATION: A NEW DISTRIBUTED TRAINING PARADIGM





- Architecture agnostic: scales to modern deep learning architectures neural view synthesis, NLP, recommender systems ...
- First optical training demonstrated on graph neural networks
 Oral presentation at NeurIPS 2020 "Beyond backprop" workshop
- Inference on silicon → model portability

At NeurIPS 2020, researchers proposed faster, more efficient alternatives to backpropagation

Kyle Wigger

@Kyle_L_Wiggers

December 16, 2020 11:18 AM

VentureBeat, Dec 2020



Scaling up



Scaling up jointly in different dimensions:

- The size of Machine Learning models - above 1M parameters models

The Hardware potential to go x100 in #OPS

towards ExaOPS Non von Neumann at less than 1 kW

- The use cases: opening to RandNLA (RandSVD → recommender systems, inverse problems...)
- Combining different building blocks with Neural Architecture Search

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Building a community





Technical articles, Blogposts, GitHub, White paper, Newsletter, Meetups ...





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Request for access to LightOn Cloud